ISSN: 2302-9285, DOI: 10.11591/eei.v14i2.7628

Three-level common emitter-current source inverter equipped with MPPT system for photovoltaic power converter

Suroso¹, Toshihiko Noguchi²

¹Department of Electrical Engineering, Faculty of Engineering, Jenderal Soedirman University, Purbalingga, Indonesia ²Department of Electrical and Electronics Engineering, Graduate School of Integrated Science and Technology, Shizuoka University, Shizuoka, Japan

Article Info

Article history:

Received Sep 28, 2023 Revised May 13, 2024 Accepted Nov 19, 2024

Keywords:

Inverter Photovoltaic Power conversion Power inductor Renewable energy

ABSTRACT

Common emitter-current source inverter (CE-CSI) has unique features with its common emitter structure of its power switches. Hence, instead of its simpler power supply for gate drive circuits, it also allows higher switching operation because of zero gradient voltage of its power switches. One of interesting applications of current source power inverter is for photovoltaic (PV) power converter. This paper discussed the three-level CE-CSI equipped with current based incremental resistance maximum power point tracking (MPPT) system as a new alternative for PV system power converter. Test results revealed some characteristics of PV power conversion using this inverter. Moreover, in order to investigate the system performance approaching real condition, testing during partial shading condition of PV modules were also conducted. Test results verified the efficacy of the incremental resistance based MPPT algorithm implemented in the CE-CSI circuits for increasing the performance of PV power generation.

This is an open access article under the CC BY-SA license.



893

Corresponding Author:

Suroso

Department of Electrical Engineering, Faculty of Engineering, Jenderal Soedirman University Jl. Mayjen Sungkono km. 5, Blater, Purbalingga, Central Java 53371, Indonesia

Email: suroso.te@unsoed.ac.id

1. INTRODUCTION

Lately, application of photovoltaic (PV) power generator has getting more popular in many countries [1]-[3]. PV are available from a small PV cell to hundreds watt capacity of a PV module. With advanced research and development, higher efficiency of PV has been developed [4]-[7]. Moreover, some governments provide incentives in the development and application of renewable energy sources such as PV. These policies have been boosting interest to apply PV in a wider and more massive usage. For many household applications, it is also very interesting to apply PV as complementary energy source. PV can be installed simply on roof top as an effective and economical technique for building and residential installations [8]-[10].

Practically, the generated power of a PV module is DC power. The output voltage and current vary regarding to environmental factors such as weather, sunlight irradiance and temperature. Power electronic converters are required to control and proceed the generated power of PV modules [11]-[13]. DC-DC power converters are employed to obtain a more stable DC voltage to be applied for DC power load system. In case of AC load system, DC-AC power converter called as power inverter is necessary to convert the generated DC power by PV into AC power [14], [15]. Using power inverter, the PV power generators can be operated in parallel with utility AC power grid in grid tied operation mode [16]-[20]. This scheme can be interesting alternative to reduce energy use from electric power utility company which are mainly generated from conventional fossil fuel power plants. Hence, it is environmentally friendly and able in reducing energy cost.

Journal homepage: http://beei.org

Voltage source inverter (VSI) has been popular and commercially available for PV power converter application. This kind of inverter proceed the DC output voltage of PV modules into specified AC voltage waveform. Power capacitor is mandatory for DC input side [21]-[23]. Another topology power inverter is referred as current-source-inverter (CSI). This inverter works changing DC current sources into a predetermined AC current waveform. Power inductor is obligatory for this inverter type to create DC current as input of inverter [24]-[26]. Compare to VSI, the current source inverter offers some advantages such as more immune to short circuit faults, faster response of its controller, longer lifetime of its inductor, voltage boosting capability, and better-quality AC current waveform. For a grid connected inverter of PV systems, it can be easily operated at high power factor operation [27]-[30].

A different circuit of three-level common-emitter CSI circuits has been presented in [28] by author. Moreover, multilevel circuit structure of this inverter has been also investigated in [29]. With common-emitter configuration of its controlled power switches, it is suitable to be operated in high switching operation such as for SiC power MOSFETs of power converters. Initial investigation of the three-level CE-CSI for PV energy converter has been done by author as discussed in [30]. However maximum power point tracking (MPPT) system have not been applied.

MPPT system is necessary to optimize energy capture by PV system [31]-[33]. One of popular MPPT algorithms is based on incremental resistance [34], [35]. This paper presents and discusses performance of three-level CE-CSI with different DC current generator circuits equipped with current based incremental resistance MPPT system. This new inverter introduced some features such as lower DC current ripple, better AC current and simpler circuit configuration because of its less DC current sensor and power switches. Some computer simulation testings were conducted to investigate characteristics and performance of inverter and MPPT system. The inverter system was also tested during partial shading of PV modules to explore the efficacy of the MPPT system.

2. PROPOSED PHOTOVOLTAIC SYSTEM

Figure 1 shows power circuit of the three-level common-emitter CSI. The inverter circuits consist of four unidirectional controlled semiconductor switches Q_1 , Q_2 , Q_3 , and Q_4 linked at common emitter configuration. Combination operation of these power switches will produce a three-level AC current signal +I, 0, and -I levels as shown in Table 1. Coupled inductors L_1 and L_2 are used to generate two DC current sources as input of inverter circuits. The power switch Q_c is used to control the current flowing thru the inductors L_1 and L_2 working as DC input current sources. In this research, the power source V_{dc} was replaced by a PV system. The diode D_f is a free-wheeling diode to maintain continuous current path of inductor currents. Capacitor filter C_f is connected at inverter's output terminal to remove harmonics components of PWM AC current waveform.

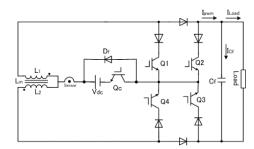


Figure 1. Three-level CE-CSI circuit [28]

$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		Table 1. Switching operation [29]							
Q1 Q2 Q3 Q4 I OFF OFF ON ON +I ON OFF OFF ON 0		Switching state				Outmut aumont			
ON OFF OFF ON 0		Q_1	Q_2	Q_3	Q_4	Output current			
011 011 011	Ī	OFF	OFF	ON	ON	+I			
ON ON OFF OFF -I		ON	OFF	OFF	ON	0			
		ON	ON	OFF	OFF	-I			

In this research, the inverter circuit was connected to 1,000 watt-peak (1 kWp) PV system consist of ten PV modules with configuration as shown in Figure 2. Table 2 presents parameters of the PV module. Five

PV modules were connected in parallel to obtain higher current from the PV system. Two parallel PV systems were connected in series to produce higher DC voltage of PV system as in Figure 2. Table 3 lists the parameters of inverter circuits applying two coupled windings with single iron core as power inductors L_1 and L_2 of inverter circuits. The switching of power inverter was chosen as 20 kHz for power MOSFET switching operation. Characteristic of output power versus voltage, and output voltage versus current of PV system for different irradiance levels are presented in Figures 3(a) and (b).

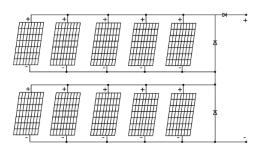


Figure 2. Configuration of PV system

Table 2. Parameters of PV module

Parameters	Values
Peak power (P _{max})	100 W
Voltage at peak power (V _{mpp})	17.46 V
Voltage (V _{oc})	21.97 V
Peak current (I _m)	5.73 A
Short circuit current (I _{sc})	6.07 A
Efficiency	12.88%

Table 3. Parameters of inverter circuits

Parameters	Values
Inductance of first inductor L ₁	0.01 H
Inductance of second inductor L ₂	0.01 H
Resistance of first inductor L ₁	0.1Ω
Resistance of second inductor L ₂	0.1Ω
Magnetizing inductance of inductor L _m	0.5 H
Switching frequency of inverter switches	20 kHz

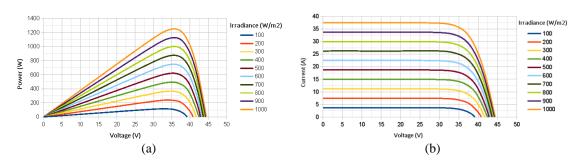


Figure 3. Characteristic of; (a) power versus voltage of PV system and (b) output voltage versus current of PV

Energy harvest using PV system depends on solar light intensity and weather condition. Hence, in order to optimize the sun light energy conversion, MPPT system was employed to the PV energy converter system. In this research, the applied MPPT system is the current based incremental resistance algorithm. Figure 4 shows the flowchart of the current based incremental resistance algorithm MPPT system implemented in CE-CSI circuits. The incremental resistance algorithm works detecting the ramp of the P-V curve, and the MPP is achieved by tracing the peak of the P-V curve. This MPPT method utilizes the instantaneous resistance *V/I* and the incremental resistance *dv/di* for MPPT [34], [35]. It depends on the values of voltage and current change as expressed in (1)-(3). Based on these conditions, the maximum operation point of the PV module can be determined from the characteristic of P-V. It will output maximum power. In (2) and (3) indicate the left and right flank operation of the maximum power from the P-V curve, successively.

$$dv/di = -V/I \tag{1}$$

$$dv/di > -V/I \tag{2}$$

$$dv/di < -V/I \tag{3}$$

These three equations are attained from the concept wherein the ramp of the P–V curve at MPP condition is equal to zero (4):

$$dp/dv = 0 (4)$$

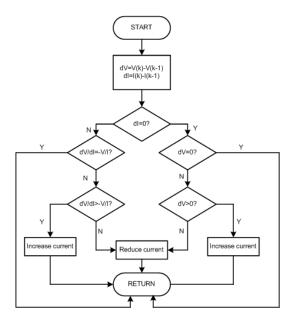


Figure 4. Flowchart of MPPT system

Based on (5) is obtained from (4),

$$I + V \frac{di}{dv} = 0 ag{5}$$

The voltage and current of the PV modules are detected and gauged by the MPPT system as shown in Figure 5. If (1) is satisfied, the duty cycle of the power converter will be reduced, and conversely if (2) is satisfied, whereas no change on the duty cycle if (3) is satisfied. The PI controller and triangle carrier signal will adjust this duty cycle after comparing them in the comparator circuit. The output of this comparator is the gating signal of the switch Q_c of inverter circuits. Moreover, the gating signal of inverter switches Q_1 , Q_2 , Q_3 , and Q_4 were generated by sinusoidal pulse width modulation method.

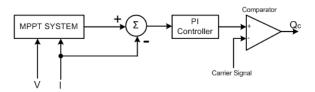


Figure 5. MPPT system

3. RESULTS AND DISCUSSION

The PV-inverter system with MPPT were tested using computer simulations to obtain the performance characteristic of inverter circuits equipped with MPPT. The test was carried out at different irradiance levels, i.e., 100 W/m^2 , 500 W/m^2 , and $1{,}000 \text{ W/m}^2$. The results are shown as follow in Figures 6(a) and (b). Figure 6(a)

presents the characteristics of output voltage (V_{pv}) , output current I_{pv} and output power of PV in case of the inverter equipped with MPPT system. While Figure 6(b) shows the characteristics without MPPT system in the inverter system. As can be seen in these 100 W/m² irradiance levels the output power was relatively small. However, if MPPT was added to inverter, the output power was larger than the inverter without MPPT.

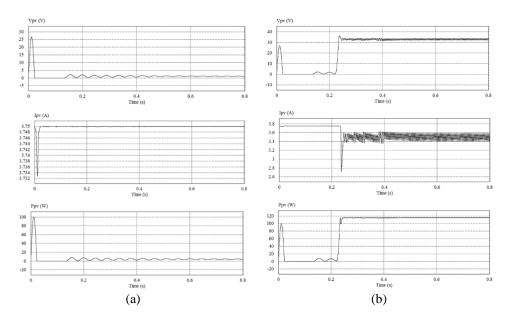


Figure 6. Comparison of output voltage, current and power of PV system: (a) without MPPT and (b) with MPPT system at irradiance $100~\text{W/m}^2$

Moreover, the test results of photovoltaic output voltage (V_{pv}) , photovoltaic current (I_{pv}) and PV output power for irradiance 500 W/m² in case of without and with MPPT are shown in Figures 7(a) and (b), respectively. An optimum output power at about 600 W was generated by the PV with MPPT system. The test results for irradiance level 1,000 W/m² of inverter system without MPPT was presented in Figure 8(a). Moreover, the test results when the inverter was equipped with MPPT was shown in Figure 8(b). Applying MPPT system, an optimum power of PV system about 1,200 W was generated.

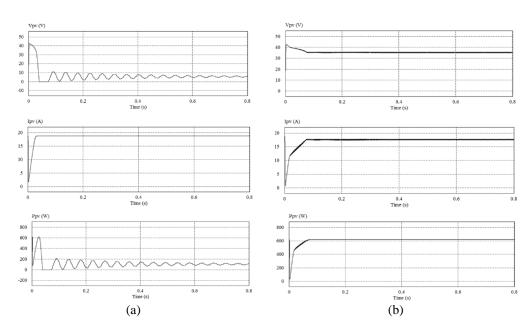


Figure 7. Comparison of output voltage, current and power of PV system: (a) without MPPT system and (b) with MPPT system at irradiance 500 W/m²

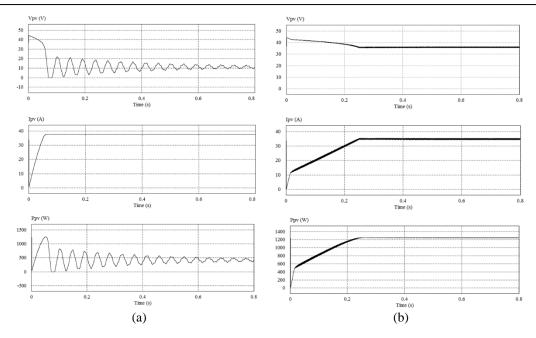


Figure 8. Comparison of output voltage, current and power of PV system: (a) without MPPT system and (b) with MPPT system at irradiance 1,000 W/m²

A comparison of output voltage, current and power of PV system with and without MPPT system when two PV modules in shading condition is depicted in Figures 9(a) and (b). The MPPT system worked well even two PV modules were in shading condition as depicted in Figure 9(b). Furthermore, the output power of PV system without and with MPPT for different power loads if two PV modules in shading condition are shown in Figure 10. The MPPT worked keeping maximum power generated by PV system. The results confirm the effectiveness of MPPT proposed to be applied in the CE-CSI circuits as PV power converter in icreasing the efficiency of power conversion. Moreover, the inverter output current before filtering (I_{PWM}) and after filtering (I_{Load}) are shown in Figure 11. The inverter generated a sinusoidal load current waveform with small waveform distortion, i.e., low harmonics component <1%.

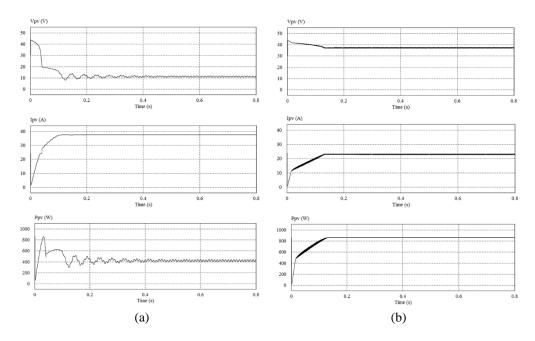


Figure 9. Comparison of output voltage, current and power of PV system: (a) without MPPT system and (b) with MPPT system when two PV modules in shading condition

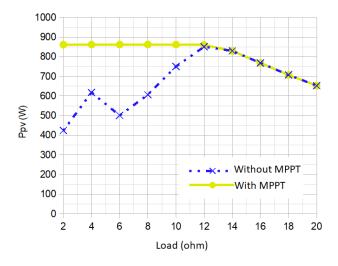


Figure 10. PV output power for different load with two PV modules in shading condition

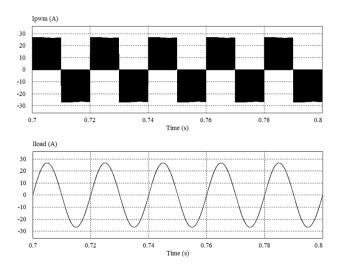


Figure 11. PWM and load current waveforms of inverter

4. CONCLUSION

This paper presented an application of incremental resistance based MPPT system in the three-level common-emitter CSI as PV power converter. The inverter worked converting the DC power of PV system into a predetermined three-level PWM AC current. The test results showed a much better performance of the inverter system equipped with MPPT system as PV power conditioner if compared to system with no MPPT. The test during shading condition of PV modules also gave a good performance of the inverter system as solar energy conversion system.

ACKNOWLEDGEMENTS

This work was funded by research grant provided by Jenderal Soedirman University, Indonesia in 2023 with contract number 27.12/UN23.37/PT.01.03/II/2023.

REFERENCES

- [1] F. Luo, G. Ranzi, C. Wan, Z. Xu, and Z. Y. Dong, "A multi-stage home energy management system with residential photovoltaic penetration," *IEEE Transactions on Industrial Informatics*, vol. 15, no. 1, pp. 116–126, 2019, doi: 10.1109/TII.2018.2871159.
- [2] A. Gholami, M. Ameri, M. Zandi, R. G. Ghoachani, S. Eslami, and S. Pierfederici, "Photovoltaic potential assessment and dust impacts on photovoltaic systems in Iran: review paper," *IEEE Journal of Photovoltaics*, vol. 10, no. 3, pp. 824–837, 2020, doi: 10.1109/JPHOTOV.2020.2978851.

[3] J. C. Blakesley et al., "Accuracy, cost and sensitivity analysis of PV energy rating," Solar Energy, vol. 203, pp. 91–100, 2020, doi: 10.1016/j.solener.2020.03.088.

- [4] H. P. Kim, K. Hee, S. J. Lee, A. R. bin M. Yusoff, and J. Jang, "Improvement of Conversion Efficiency of Inverted Organic Photovoltaic With PEDOT: PSS: WOx by Thermal Annealing," *IEEE Journal of Photovoltaics*, vol. 5, no. 3, May 2015, pp. 897-902, doi: 10.1109/JPHOTOV.2015.2405754.
- [5] K. Lee, J. Y. Lee, S. Seo, and L. J. Guo, "Microcavity-Integrated Colored Semitransparent Hybrid Photovoltaics With Improved Efficiency and Color Purity," *IEEE Journal of Photovoltaics*, vol. 5, no. 6, 2015, pp. 1654-1658, doi: 10.1109/JPHOTOV.2015.2478063.
- [6] J. F. Geisz et al., "Building a six-junction inverted metamorphic concentrator solar cell," IEEE Journal of Photovoltaics, vol. 8, no. 2, pp. 626–632, 2018, doi: 10.1109/JPHOTOV.2017.2778567.
- [7] J. Jurasz, F. A. Canalesc, A. Kiesd, M. Guezgouze, and A. Belucof, "A review on the complementarity of renewable energy sources: Concept, metrics, application and future research directions," *Solar Energy*, vol. 195, pp. 703-724, 2020, doi: 10.1016/j.solener.2019.11.087.
- [8] Z. Zhou *et al.*, "Vortex generators for passive cooling of rooftop photovoltaic systems under free convection," *IEEE Journal of Photovoltaics*, vol. 13, no. 5, 2023, pp. 743–749, doi: 10.1109/JPHOTOV.2023.3299752.
- [9] M. H. Saw, J. P. Singh, Y. Wang, K. E. Birgersson, and Y. S. Khoo, "Electrical performance study of colored c-Si building-integrated PV modules," *IEEE Journal of Photovoltaics*, vol. 10, no. 4, 2020, pp. 1027-1034, doi: 10.1109/JPHOTOV.2020.2981820.
- [10] A. Gok, E. Ozkalay, G. Friesen, and F. Frontini, "Power loss modes of building-integrated photovoltaic modules: an analytical approach using outdoor I-V curves," *IEEE Journal of Photovoltaics*, vol. 11, no. 3, 2021, pp. 789-796, doi: 10.1109/JPHOTOV.2021.3060719.
- [11] R. Panigrahi, S. K. Mishra, S. C. Srivastava, A. K. Srivastava, and N. N. Schulz, "Grid integration of small-scale photovoltaic systems in secondary distribution network a review," *IEEE Transactions on Industry Applications*, vol. 56, no. 3, pp. 3178-3195, 2020, doi: 10.1109/TIA.2020.2979789.
- [12] K. Alluhaybi, I. Batarseh, and H. Hu, "Comprehensive review and comparison of single-phase grid-tied photovoltaic microinverters," *IEEE Journal of Emerging and Selected Topics in Power Electronics*, vol. 8, no. 2, pp. 1310–1329, 2020, doi: 10.1109/JESTPE.2019.2900413.
- [13] J. M. Carrasco, L. G. Franquelo, J. T. Bialasiewicz, and N. M. Alfonso, "Power-electronics systems for the grid integration of renewable energy sources: a survey," *IEEE Transactions on Industrial Electronics*, vol. 53, pp. 1002-1016, 2008, doi: 10.1109/TIE.2006.878356.
- [14] N. Vazquez, H. Lopez, C. Hernandez, E. Vazquez, R. Osorio, and J. Arau, "A different multilevel current source inverter," *IEEE Transactions on Industrial Electronics*, vol. 57, no. 8, pp. 2623–2632, 2010, doi: 10.1109/TIE.2009.2030814.
- [15] K. Gnanasambandam, A. K. Rathore, A. Edpuganti, D. Srinivasan, and J. Rodriguez, "Current-fed multilevel converters: an overview of circuit topologies, modulation techniques, and applications," *IEEE Transactions on Power Electronics*, vol. 32, no. 5, pp. 3382-3401, 2016, doi: 10.1109/TPEL.2016.2585576.
- [16] P. G. Barbosa, H. A. C. Braga, M. C. Barbosa, and E. C. Teixeria, "Boost current multilevel inverter and its application on single phase grid connected photovoltaic system," *IEEE Transactions on Power Electronics*, vol. 21, no. 4, pp. 1116-1124, 2006, doi: 10.1109/TPEL.2006.876784.
- [17] A. A. A. Radwan and Y. A. I. Mohamed, "Power synchronization control for grid-connected current-source inverter-based photovoltaic systems," *IEEE Transactions on Energy Conversion*, vol. 31, no. 3, pp. 1023-1036, 2016, doi: 10.1109/TEC.2016.2533630.
- [18] X. Guo, N. Wang, J. Zhang, B. Wang, and M. Nguyen, "A novel transformerless current source inverter for leakage current reduction," *IEEE Access*, vol. 7, pp. 50681-50690, 2019, doi: 10.1109/ACCESS.2019.2908287.
- [19] Z. Wang, Z. Zou, and Y. Zheng, "Design and control of a photovoltaic energy and SMES hybrid system with current-source grid inverter," *IEEE Transactions on Applied Superconductivity*, vol. 23, no. 3, 5701505, 2013, doi: 10.1109/TASC.2013.2250172.
- [20] S. B. Kjaer, J. K. Pedersen, and F. Blaabjerg, "A review of single-phase grid-connected inverters for photovoltaic modules," IEEE Transactions on Industry Applications, vol. 41, no. 5, pp. 1292-1306, 2005, doi: 10.1109/TIA.2005.853371.
- [21] Suroso, A. N. Aziz, and T. Noguchi, "Five-level PWM inverter with a single DC power source for DC-AC power conversion," International Journal of Power Electronics and Drive Systems, vol. 8, no. 3, pp. 1230-1237, 2017, doi: 10.11591/ijpeds.v8.i3.pp1212-1219.
- [22] N. Thombre, P. Rana, R. S. Rawat, and Umashankar S, "A novel topology of multilevel inverter with reduced number of switches and DC sources," *International Journal of Power Electronics and Drive System*, vol. 5, no. 1, pp. 56-62, 2014, doi: 10.11591/ijpeds.v4i1B.5810
- [23] R. S. Alishah and S. H. Hosseini, "A new multilevel inverter structure for high-power applications using multi-carrier PWM switching strategy," International Journal of Power Electronics and Drive System, vol. 6, no. 2, pp. 318-325, 2015, doi: 10.11591/ijpeds.v6.i2.pp318-325.
- [24] F. Marignetti, R. L. Di Stefano, G. Rubino, and R. Giacomobono, "Current source inverter (CSI) power converters in photovoltaic systems: a comprehensive review of performance, control, and integration, *Energies*, vol. 16, 7319, 2023, doi: 10.3390/en16217319.
- [25] E. Lorenzani, F. Immovilli, G. Migliazza, M. Frigieri, C. Bianchini, and M. Davoli, "CSI7: a modified three-phase current source inverter for modular photovoltaic applications," *IEEE Transactions on Industrial Electronics*, vol. 64, no. 7, pp. 5449 – 5459, 2017, doi: 10.1109/TIE.2017.2674595.
- [26] Z. H. Bai and Z. C. Zhang, "Conformation of multilevel current source converter topologies using the duality principle," *IEEE Transactions on Power Electronics*, vol. 23, no. 5, pp. 2260-2267, 2008, doi: 10.1109/TPEL.2008.2001893.
- [27] Suroso, Winasis, Priswanto, and Sholikhah. "Improving output current of inductor-cell based five-level CSI using hysteresis current controller," International Journal of Power Electronics and Drive Systems, vol. 12, no. 1, pp. 249-257, 2021, doi: 10.11591/ijpeds.v12.i1.pp249-257
- [28] Suroso, D. T. Nugroho, and Winasis, "Three-level common-emitter current-source power inverter with simplified DC current-source generation," *Journal of Engineering Science and Technology*, vol. 13, no. 12, pp. 4027-4038, 2018.
- [29] Suroso and T. Noguchi, "Common-emitter topology of multilevel current-source pulse width modulation inverter with chopper-based DC current sources," *IET Power Electronics*, vol. 4, no.7, pp. 759-766, 2011, doi: 10.1049/iet-pel.2010.0008.
- [30] Suroso and T. Noguchi, "A new three-level current-source PWM inverter and its application for grid connected power conditioner," *Energy Conversion and Management*, vol. 51, no. 7, pp. 1491-1499, 2010, doi: 10.1016/j.enconman.2010.02.007.

П

- [31] N. Mutoh, M. Ohno, and T. Inoue, "A method for MPPT control while searching for parameters corresponding to weather conditions for PV generation systems," *IEEE Transactions on Industrial Electronics*, vol. 53, no. 4, pp. 1055-1065 2006, doi: 10.1109/TIE.2006.878328.
- [32] S. R. Chowdhury and H. Saha, "Maximum power point tracking of partially shaded solar photovoltaic arrays," Solar Energy Materials & Solar Cells, vol. 94, pp. 1441-1447, 2010, doi: 10.1016/j.solmat.2010.04.011.
- [33] A. Bidram, A. Davoudi, and R. S. Balog, "Control and circuit techniques to mitigate partial shading effects in photovoltaic arrays," *Journal of Photovoltaics*, vol. 2, no. 4, 2012, doi: 10.1109/JPHOTOV.2012.2202879.
- [34] E. M. Ahmed and M. Shoyama, "Scaling factor design based variable step size incremental resistance maximum power point tracking for PV systems," *Journal of Power Electronics*, vol. 12, no. 1, 2012, doi: 10.6113/JPE.2012.12.1.164.
- [35] Q. Mei, M. Shan, L. Liu, and J. M. Guerrero, "A novel improved variable step-size incremental-resistance MPPT method for PV systems," *IEEE Transactions on Industrial Electronics*, vol. 58, no. 6, 2011, doi: 10.1109/TIE.2008.920550.

BIOGRAPHIES OF AUTHORS



Suroso (D) (S) (S) (C) received the B.Eng. degree in electrical engineering, from Gadjah Mada University, Indonesia in 2001, and the M.Eng. degree in electrical and electronics engineering from Nagaoka University of Technology, Japan in 2008. He was a research student at Department of Electrical Engineering, Tokyo University, Japan from 2005 to 2006. He earned the Ph.D. degree in Department of Energy and Environment Engineering, Nagaoka University of Technology, Japan in 2011. He was a visiting researcher at Department of Electrical and Electronics Engineering, Shizuoka University, Japan from 2009 to 2011. Currently, he is a professor at Department of Electrical Engineering, Jenderal Soedirman University, Purwokerto, Central Java, Indonesia. His research interest includes static power converters and its application in renewable energy conversion system. He can be contacted at email: suroso.te@unsoed.ac.id.



Toshihiko Noguchi was born in 1959. He received the B.Eng. degree in electrical engineering from Nagoya Institute of Technology, Nagoya, Japan, and the M.Eng. and D.Eng. degrees in electrical and electronics systems engineering from Nagaoka University of Technology, Nagaoka, Japan, in 1982, 1986, 1996, respectively. In 1982, he joined Toshiba Corporation, Tokyo, Japan. He was a Lecturer at Gifu National College of Technology, Gifu, Japan, from 1991 to 1993 and a Research Associate in electrical and electronics systems engineering at Nagaoka University of Technology from 1994 to 1995. He was an Associate Professor at Nagaoka University of Technology from 1996 to 2009. He has been a Professor at Shizuoka University since 2009. His research interests are static power converters and motor drives. He can be contacted at email: noguchi.toshihiko@shizuoka.ac.jp.